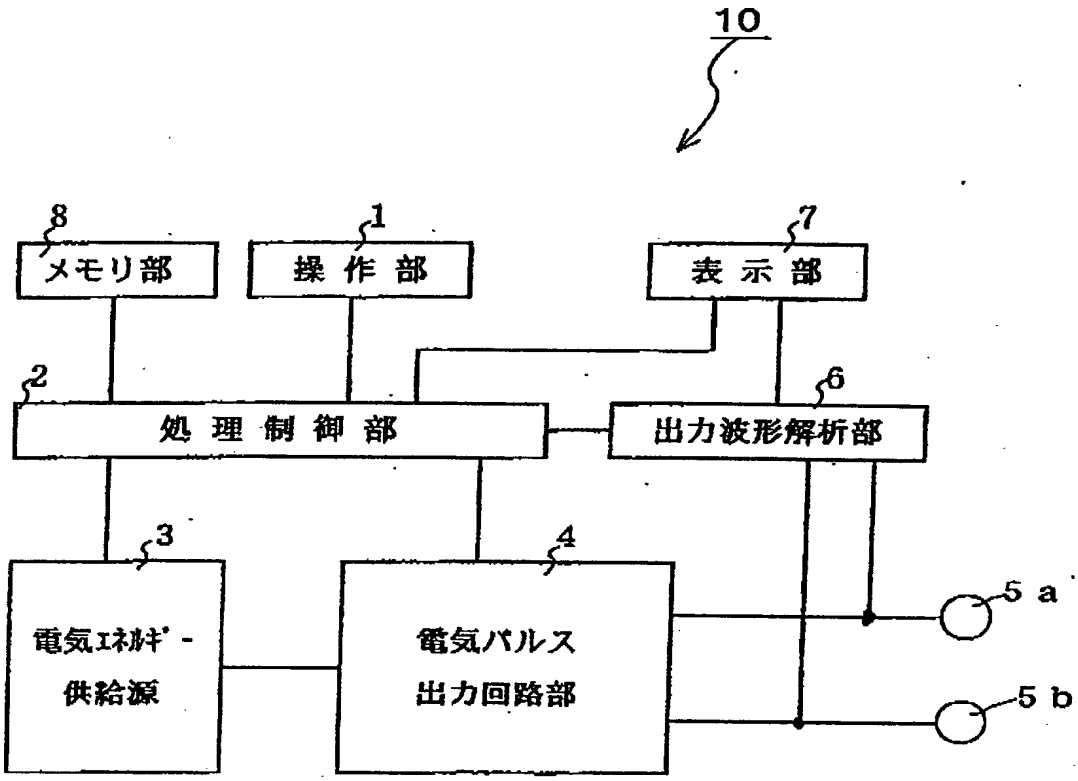
Fig. 1



1: control panel
2: processor
3: power source
4: pulse generator
6: output waveform analyzer
7: screen
8: storage

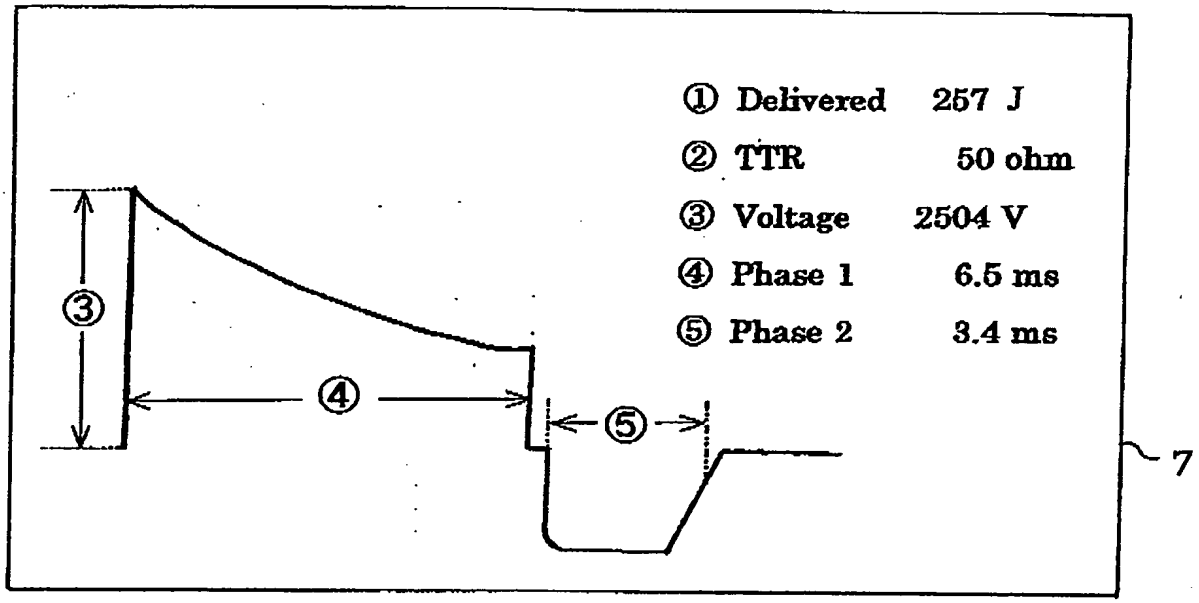

① Delivered 257 J
② TTR 50 ohm
③ Voltage 2504 V
④ Phase 1 6.5 ms
⑤ Phase 2 3.4 ms
③
④
⑤
7

Fig. 2

Fig. 3

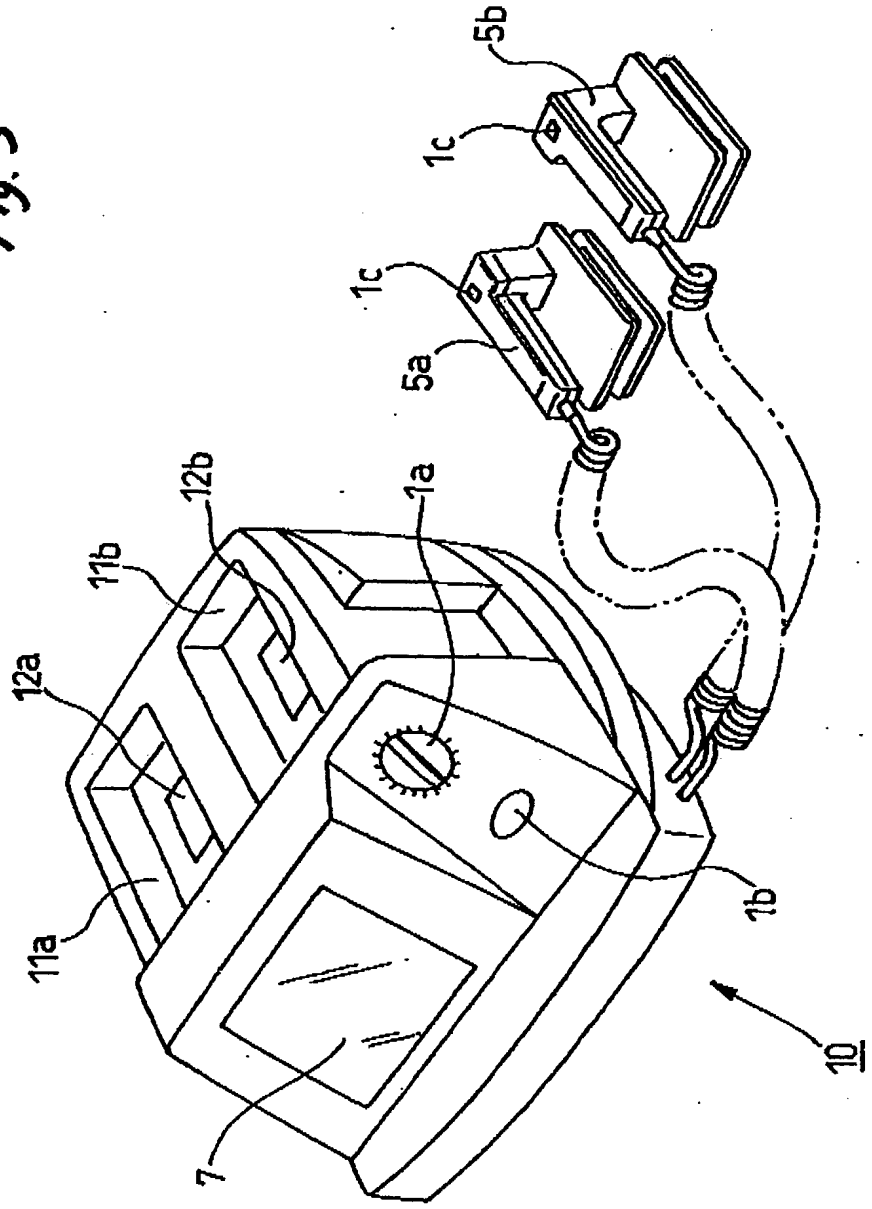
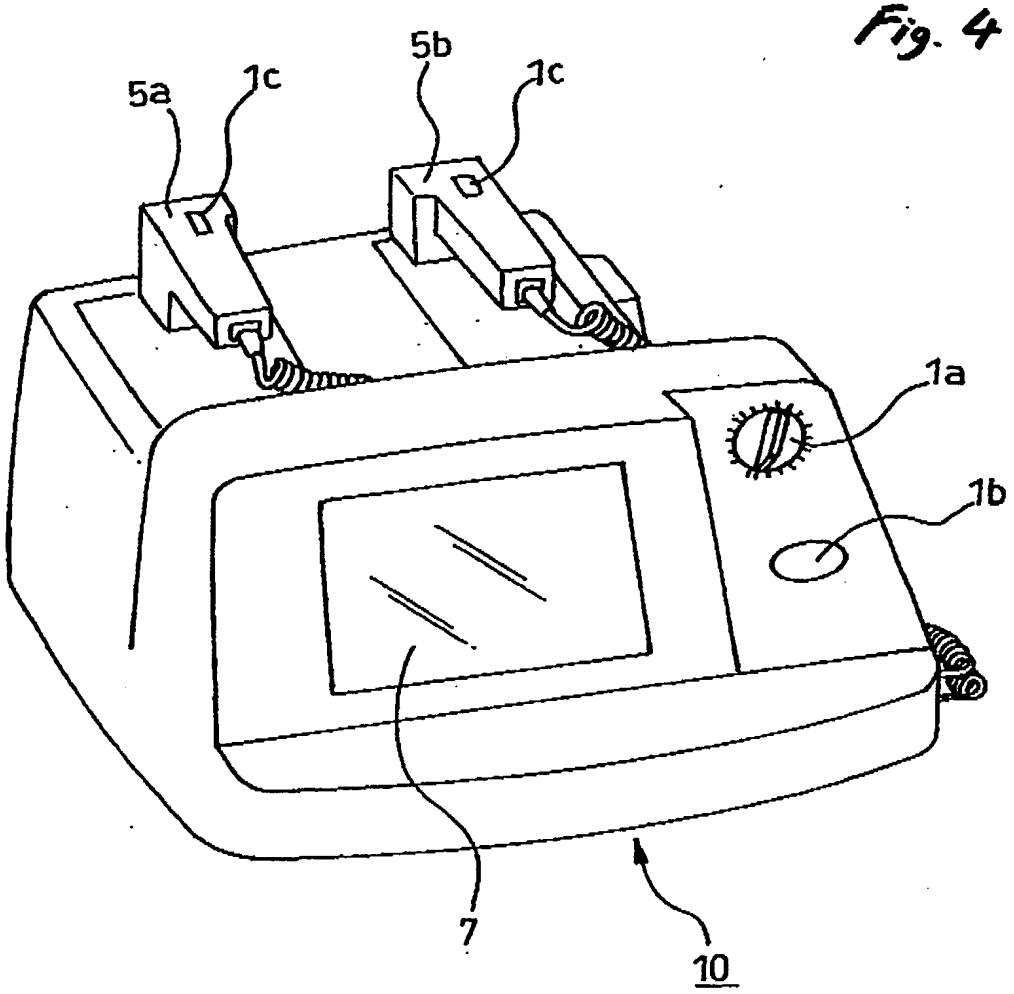
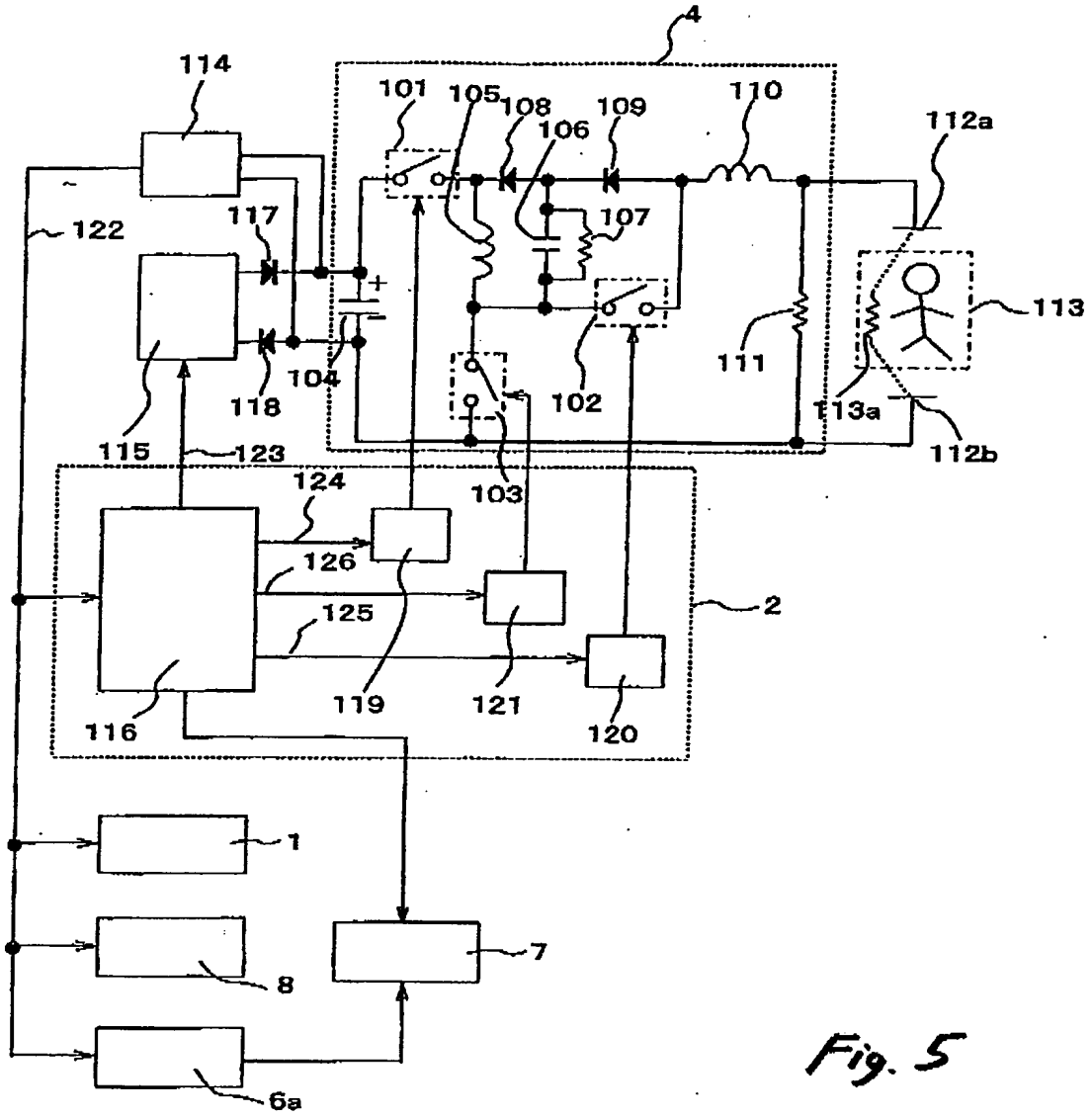


Fig. 4

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112b
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121
120
1
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Fig. 5

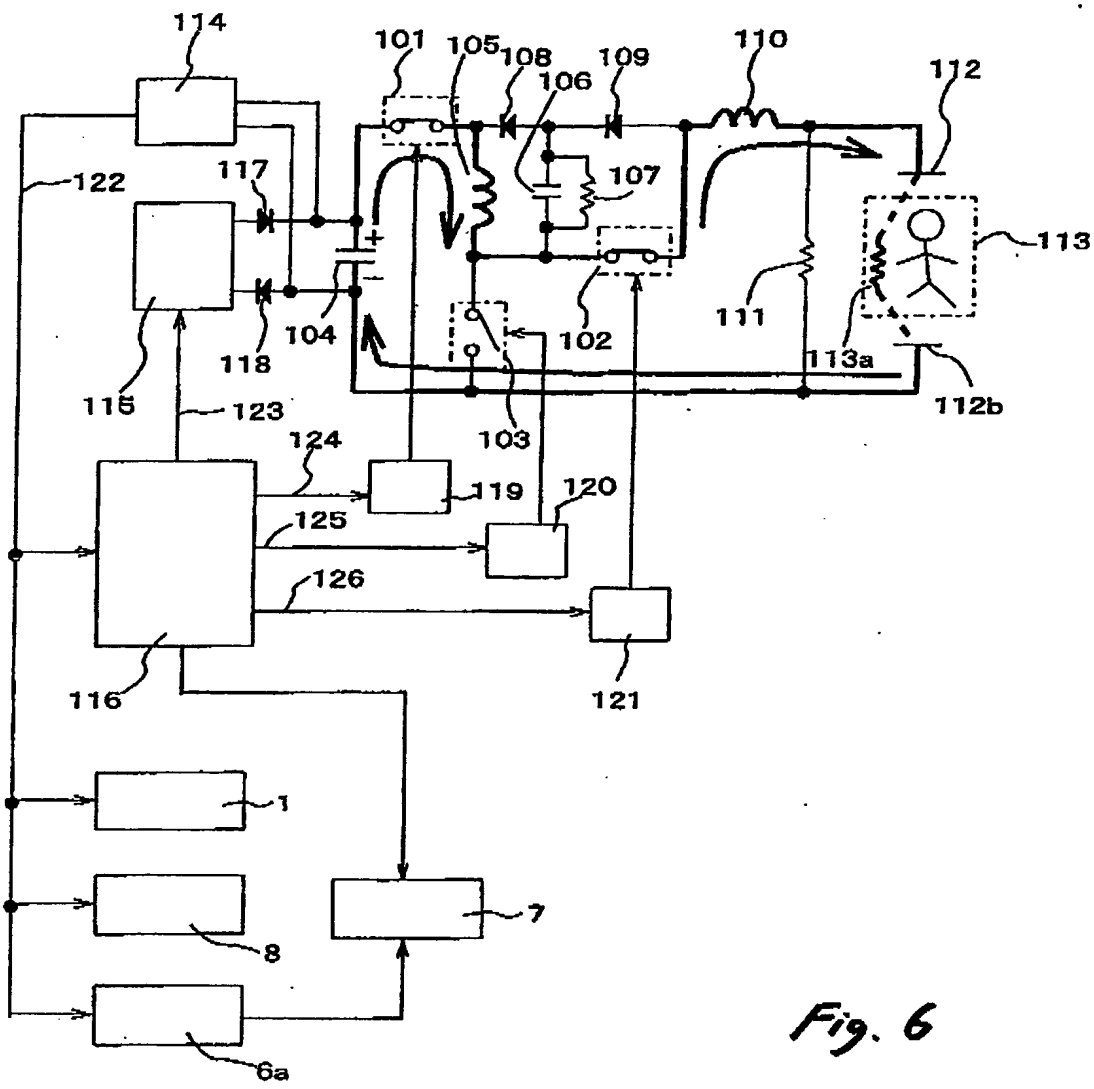
114
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Fig. 6

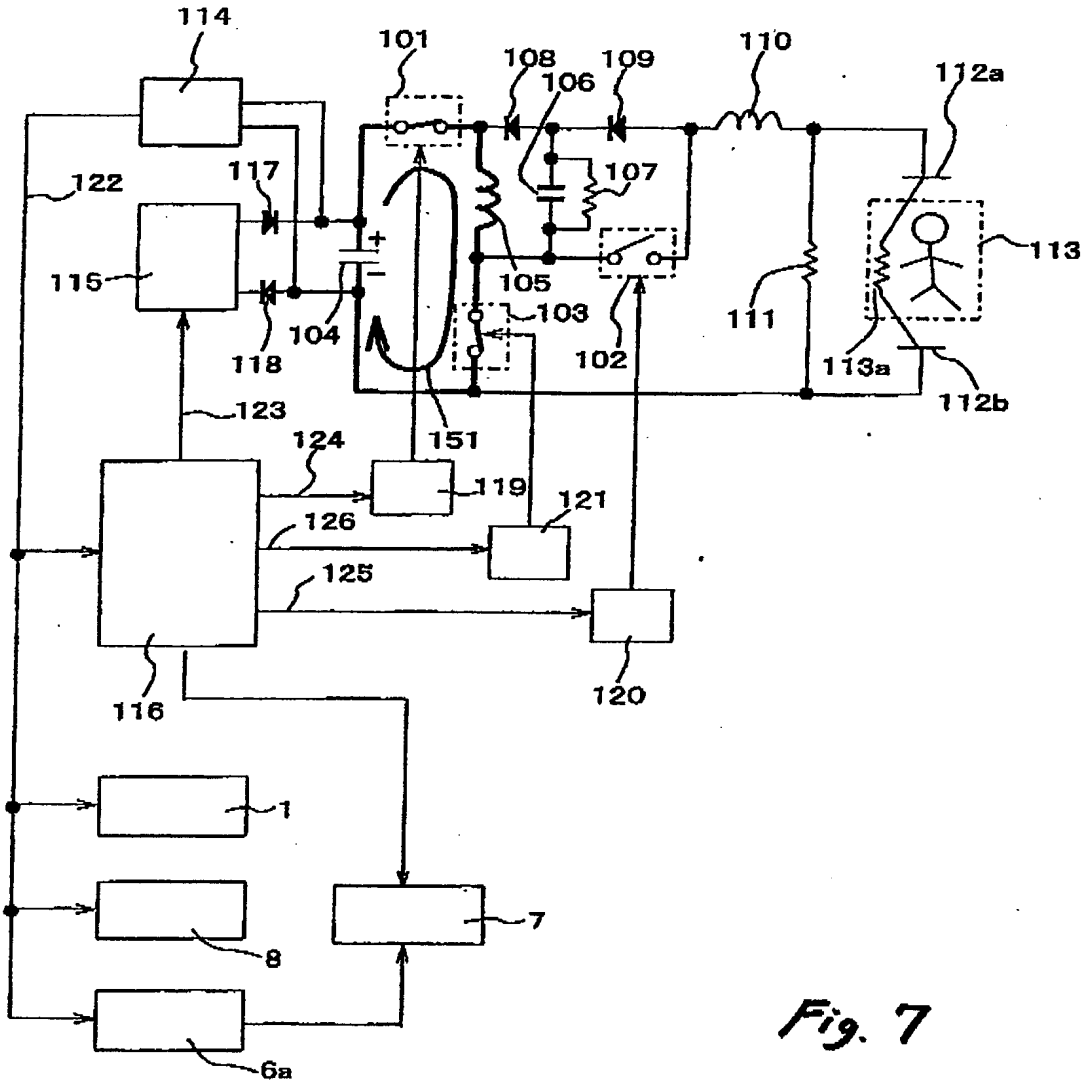
114
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Fig. 7

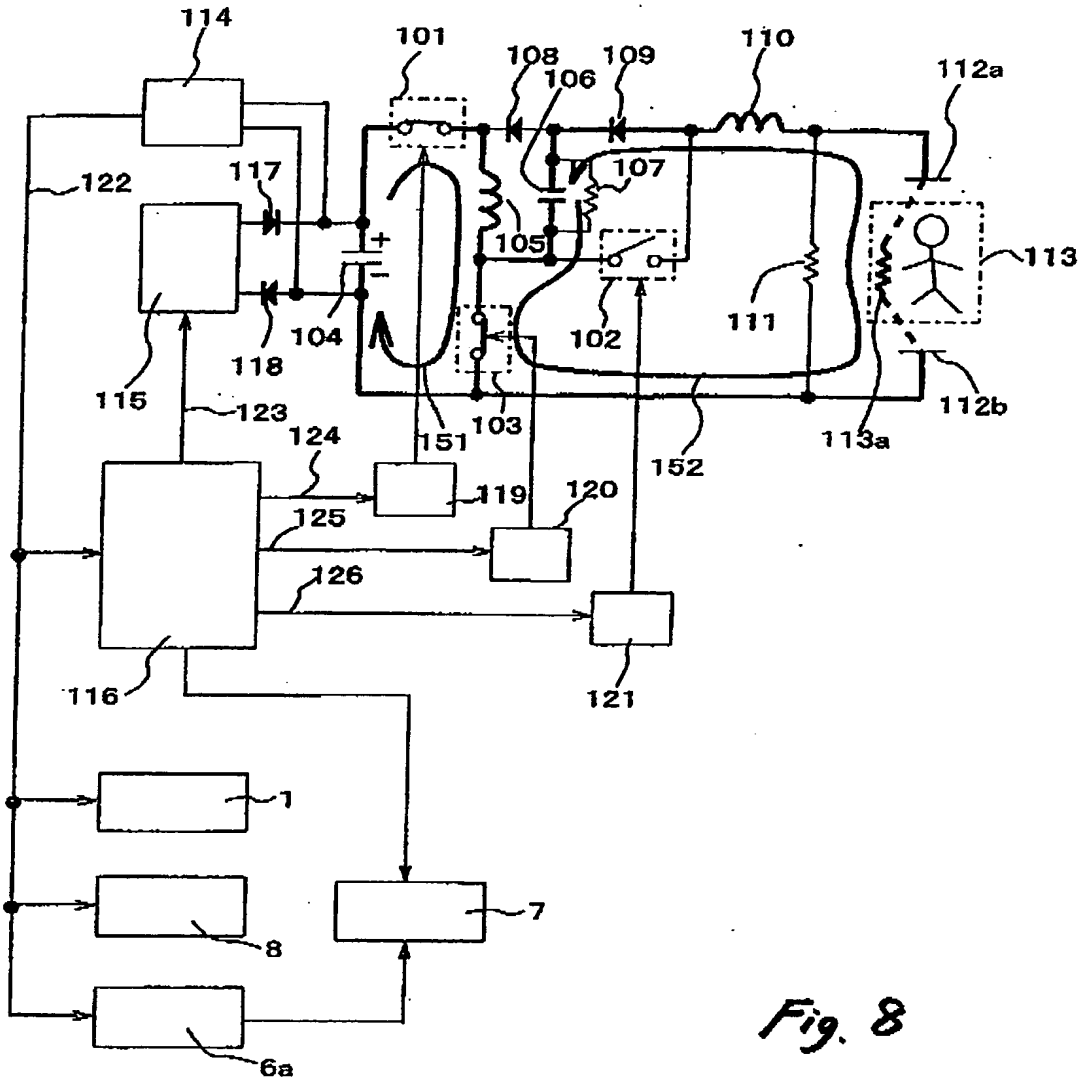
114
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Fig. 8

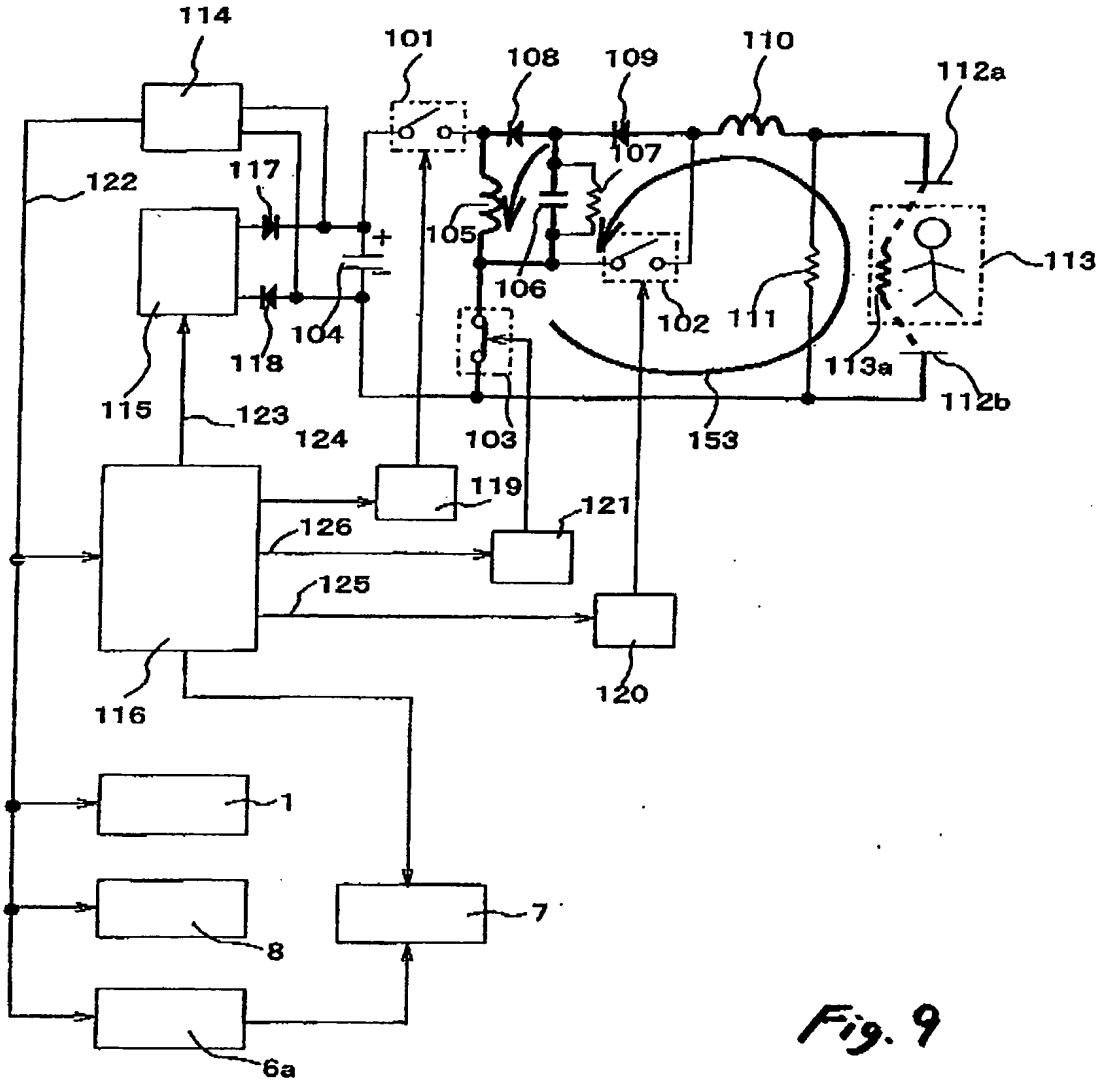
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Fig. 9

Fig. 10A

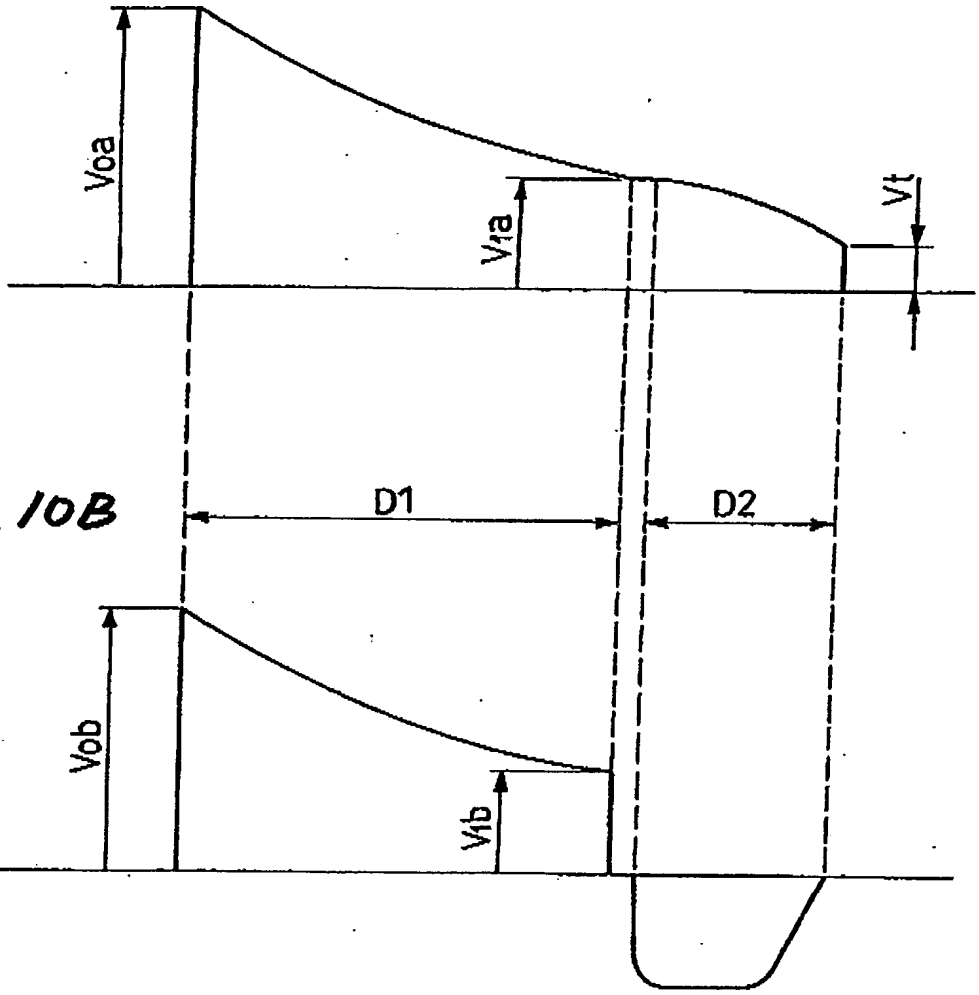

Fig. 10B